

**ECR #: P5**

**Tracker #: 8**

**Status: Ratified**

**Title: Change I<sup>2</sup>C Bus Signal Type in Table 4.11 to Open Drain**

**Release Date: June 2, 1997**

**Impact: Possible Impact to Current Motherboard Designs**

**Spec Version: NLX 1.2**

**Summary:**

In Table 4.11 on page 47, the signal type for SDA and SCL needs to be changed to Open Drain for compatibility with SM Bus implementations. In the table, only this signal type is changed, and only the underlined portion is changed.

**Background:**

A request was made to change the signal type for the SDA and SCL signals from TTL, as they are currently specified, to Open Drain. This would allow compatibility with SM bus implementations.

**Change Current Specification As Shown:**

In Table 4.11 on page 47, correct the description of the Serial bus signal as follows:

| Signal     | Pin  | I/O | Description  | Signal Type       |
|------------|------|-----|--|-------------------|
|            |      |     |  |                   |
| Serial bus |      |     |  |                   |
| SDA        | A150 | I/O | serial data signal.  | <u>Open Drain</u> |
| SCL        | A151 | O   | serial clock signal.<br><br>Note: That the NLX specification reserves EEPROM binary device address 1010000 for future riser configuration initiatives. | <u>Open Drain</u> |
|            |      |     |  |                   |